

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: Wen-Chi WANG et. al.

Serial No: Unassigned

Filed: December 2, 2003

For: Line Driver With Active Termination

) Atty Dkt: WANG3209/EM
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Assistant Commissioner of Patents
and Trademarks
Alexandria, Virginia 22313-1450

INFORMATION DISCLOSURE STATEMENT

Sir:

Pursuant to Rule 37 C.F.R. §1.51(b), §1.56, §1.97, and §1.98, this Information Disclosure Statement is submitted in the above-identified patent application. A listing of documents to be published on the face of any patent granted from this application is submitted herewith on Form PTO-1449. Any other documents or information submitted for consideration by the Examiner are listed in this paper.

The Examiner is requested to acknowledge consideration of the information provided in this paper in accordance with prescribed procedures.

Please charge any additional fees or credit any overpayments in connection with this paper to Deposit Account No. 02-0200.

Respectfully submitted,



Eugene Mar
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Date: December 2, 2003

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B/O Form PTO-1449 U.S. Department of Commerce Patent and Trademark Office Information Disclosure Statement by Applicant	Atty. Docket Number WANG3209/EM	Serial Number Unassigned
	Applicant Wen-Chi WANG et. al.	
	Filing Date Concurrently Herewith - Dec. 1, 2003	Group Unassigned

U.S. Patent Documents

Examiner Initial	Document Number	Date	Patentee/Applicant	Class	Subclass	Filing Date if Appropriate
	6,522,179 B2	02/18/2003	Ferianz			09/14/2001
	6,314,180 B1	11/06/2001	Bingel			03/16/1998
	5,121,080	06/09/1992	Scott, III. et. al.			12/21/1990
	4,798,982	01/17/1989	Voorman			09/08/1987

Foreign Patent Documents

Examiner Initial	Document Number	Publication Date	Country/Agency	Class	Subclass	Translation	
						Yes	No

Other Documents (Including Author, Title, Date, Pertinent Pages, Place of Publication, Etc.)

		Bram Nauta et. al., <i>Analog Line Driver with Adaptive Impedance Matching</i> , IEEE Journal of Solid-State Circuits, Vol. 33, No. 12, December 1998, pp. 1992-1998.
		M. Corsi et. al., <i>An ADSL Central Office Analog Front-End Integrating Actively-Terminated Line Driver, Receiver and Filters.</i> , 2001 IEEE International Solid-State Circuits Conference.
		Faramarz Sabouri et. al., <i>A 740mW ADSL Line Driver for Central Office with 75dB MTPR</i> , 2002 IEEE International Solid-State Circuits Conference

Examiner	Date Considered
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EXAMINER: Initial if citation is considered, whether or not citation is in conformance with MPEP 609; Draw a line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.